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Frank Vahid is the author of Digital Design with RTL Design, VHDL, and Verilog, 2nd Edition, published by Wiley.

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1 – Soda dispenser example – Not an FSM

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because: • Multi-bit (data) inputs a and s •
Local register tot • Data operations tot=0,
tot<s, tot=tot+a. – Useful high-level state
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