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Architecture Multiple Choice Questions

- :-. 1. In Reverse Polish notation, expression $A*B+C*D$ is written as. 2. SIMD represents an organization that _____ . time. unit and a memory unit. (D) none of the above. 3. Floating point representation is used to store. 4. ...

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include the collections of objective questions on the fundamentals of computer organization and architecture. 1. Memory unit that communicates directly with the CPU is called the A) Main memory B) Secondary memory C) Auxiliary memory D) Register. 2.

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Questions With Answers :- 1. In Reverse Polish notation, expression $A*B+C*D$ is written as. (A) $AB*CD*+$ (B) $A*BCD*+$ (C) $AB*CD+*$ (D) $A*B*CD+.$

Ans: A. 2. SIMD represents an organization that _____. (A) refers to a

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Question 1 In a k-way set associative

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Cache, the cache is divided into v sets, each of which consists of k lines. The lines of a set are placed in sequence one after another. The lines in set s are sequenced before the lines in set $(s+1)$.

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of lines that connects several devices
is called _____.. A) bus B) peripheral
connection wires C) Both a and b D)
internal wires. 2.

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52. If in a computer, 16 bits are used

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Questions With Answers
to specify address in a RAM, the
number of addresses will be A) 216 B)
65,536 C) 64K D) Any of the above.
53.

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